

# PHOTOCOUPLER PS2501A-1,PS2501AL-1

# HIGH ISOLATION VOLTAGE SINGLE TRANSISTOR TYPE MULTI PHOTOCOUPLER SERIES

-NEPOC Series-

#### **DESCRIPTION**

The PS2501A-1 and PS2501AL-1 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon phototransistor to realize an excellent cost performance.

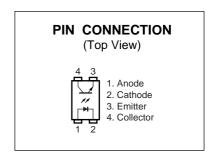
The PS2501A-1 is in a plastic DIP (Dual In-line Package) and the PS2501AL-1 is lead bending type (Gull-wing) for surface mount.

#### **FEATURES**

- High isolation voltage (BV = 5 000 Vr.m.s.)
- Ordering number of taping product: PS2501AL-1-E3, E4, F3, F4
- Pb-Free product
  - · Safety standards
    - UL approved: File No. E72422

#### **APPLICATIONS**

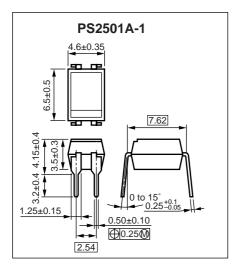
- Power supply
- Telephone/FAX.
- FA/OA equipment
- · Programmable logic controller



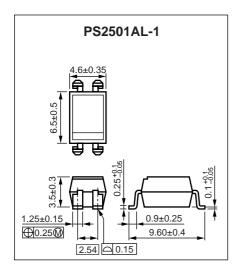
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# PACKAGE DIMENSIONS (UNIT: mm)

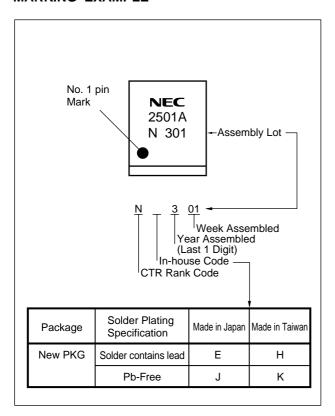
# **DIP Type**



# **Lead Bending Type**



# **MARKING EXAMPLE**



# **★ ORDERING INFORMATION**

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number *1
PS2501A-1	PS2501A-1	Solder	Magazine case 100 pcs	Standard products	PS2501A-1
PS2501AL-1	PS2501AL-1	contains lead		(UL Approved)	
PS2501AL-1-E3	PS2501AL-1-E3		Embossed Tape 1 000 pcs/reel		
PS2501AL-1-E4	PS2501AL-1-E4				
PS2501AL-1-F3	PS2501AL-1-F3		Embossed Tape 2 000 pcs/reel		
PS2501AL-1-F4	PS2501AL-1-F4				
PS2501A-1	PS2501A-1-A	Pb-Free	Magazine case 100 pcs		
PS2501AL-1	PS2501AL-1-A				
PS2501AL-1-E3	PS2501AL-1-E3-A		Embossed Tape 1 000 pcs/reel		
PS2501AL-1-E4	PS2501AL-1-E4-A				
PS2501AL-1-F3	PS2501AL-1-F3-A		Embossed Tape 2 000 pcs/reel		
PS2501AL-1-F4	PS2501AL-1-F4-A				

<sup>\*1</sup> For the application of the Safety Standard, following part number should be used.

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

	Parameter	Symbol	Ratings	Unit
Diode	Reverse Voltage	VR	6	V
	Forward Current (DC)	lF	30	mA
	Power Dissipation Derating	⊿P₀/°C	1.5	mW/°C
	Power Dissipation	PD	150	mW
	Peak Forward Current*1	IFP	0.5	Α
Transistor	Collector to Emitter Voltage	Vceo	70	V
	Emitter to Collector Voltage	Veco	5	V
	Collector Current	lc	30	mA
	Power Dissipation Delay	⊿Pc/°C	1.5	mW/°C
	Power Dissipation	Pc	150	mW
Isolation Voltage*2		BV	5 000	Vr.m.s.
Operating Ambient Temperature		TA	-55 to +100	°C
Storage Temperature		T <sub>stg</sub>	-55 to +150	°C

<sup>\*1</sup> PW = 100  $\mu$ s, Duty Cycle = 1%

<sup>\*2</sup> AC voltage for 1 minute at  $T_A = 25$ °C, RH = 60% between input and output

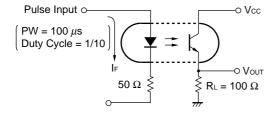
# **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA		1.2	1.4	V
	Reverse Current	lr	V <sub>R</sub> = 5 V			5	μА
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		10		pF
Transistor	Collector to Emitter Dark Current	Iceo	VcE = 70 V, IF = 0 mA			100	nA
Coupled	Current Transfer Ratio	CTR	IF = 5 mA, VcE = 5 V	50		400	%
	Collector Saturation Voltage	VCE (sat)	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 2 mA		0.13	0.3	V
	Isolation Resistance	R <sub>I-O</sub>	Vi-o = 1.0 kVpc	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1.0 MHz		0.4		pF
	Rise Time*2	tr	$Vcc = 10 \text{ V}, \text{ Ic} = 2 \text{ mA}, \text{ R}_L = 100 \Omega$		5		μS
	Fall Time*2	tf			7		

#### \*1 CTR rank

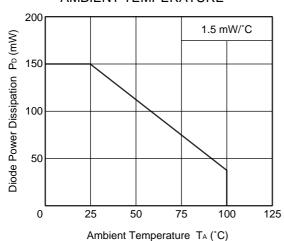
N : 50 to 400 (%) H : 80 to 160 (%) W : 130 to 260 (%) Q : 100 to 200 (%) L : 200 to 400 (%)

# \*2 Test circuit for switching time

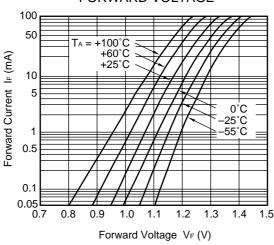


## TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)

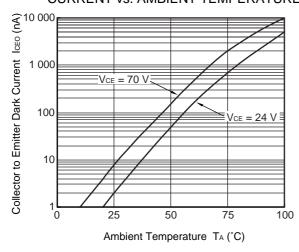
# DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



## FORWARD CURRENT vs. FORWARD VOLTAGE

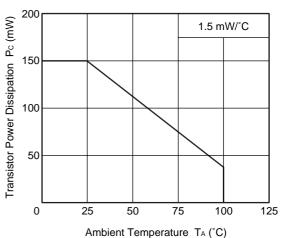


# COLLECTOR TO EMITTER DARK **CURRENT vs. AMBIENT TEMPERATURE**

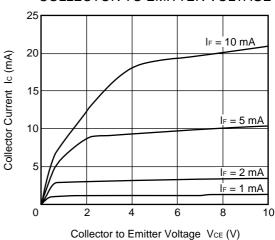


# **Remark** The graphs indicate nominal characteristics.

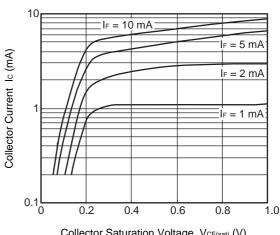
## TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



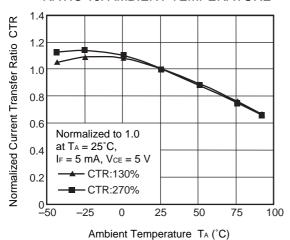
# COLLECTOR CURRENT vs. **COLLECTOR TO EMITTER VOLTAGE**



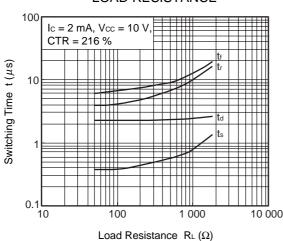
# COLLECTOR CURRENT vs. **COLLECTOR SATURATION VOLTAGE**



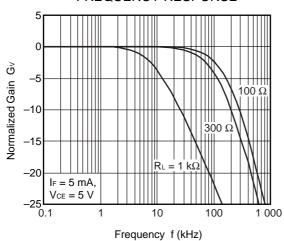
# ★ NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



# SWITCHING TIME vs. LOAD RESISTANCE

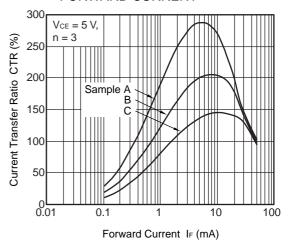


# FREQUENCY RESPONSE

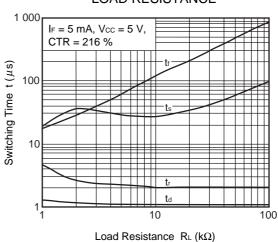


**Remark** The graphs indicate nominal characteristics.

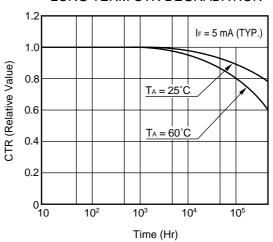
# CURRENT TRANSFER RATIO vs. FORWARD CURRENT



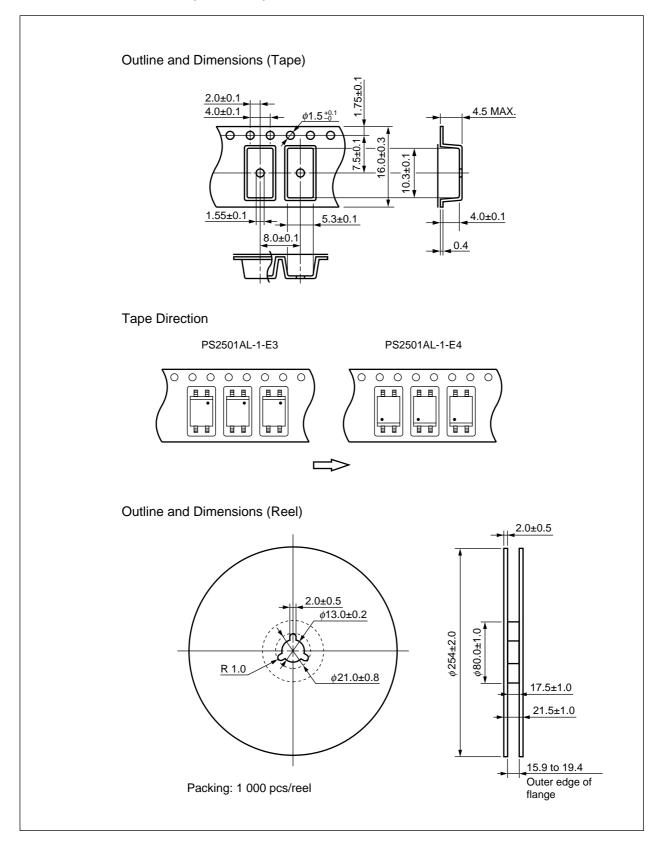
# SWITCHING TIME vs. LOAD RESISTANCE

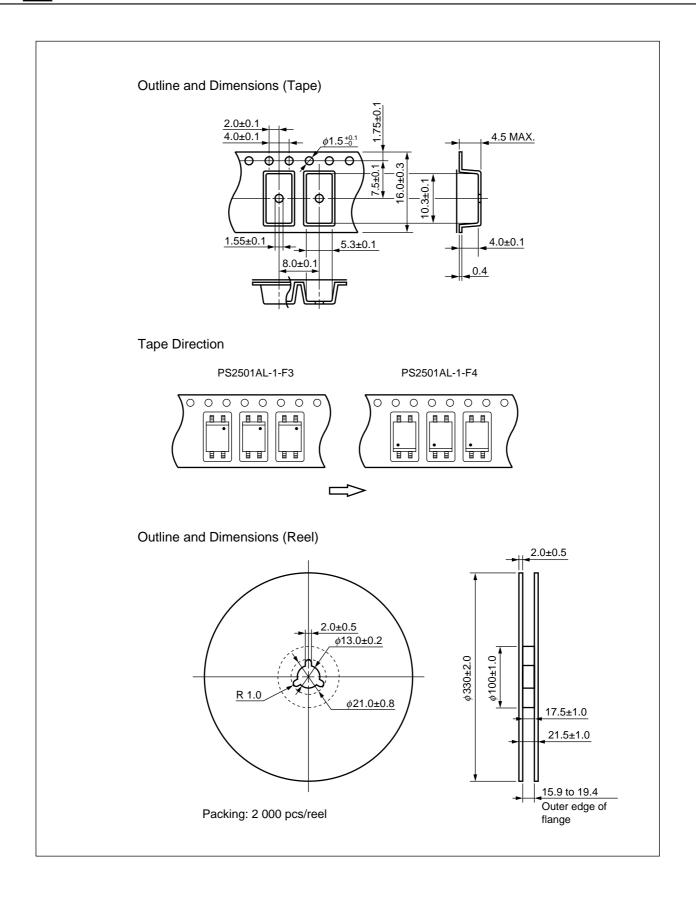


#### LONG TERM CTR DEGRADATION



# TAPING SPECIFICATIONS (UNIT: mm)





#### NOTES ON HANDLING

# 1. Recommended soldering conditions

## (1) Infrared reflow soldering

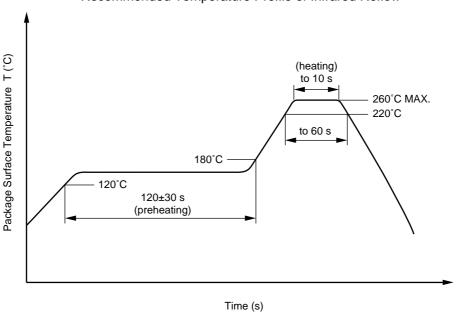
• Peak reflow temperature 260°C or below (package surface temperature)

Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

#### Recommended Temperature Profile of Infrared Reflow



#### (2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

• Number of times One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

#### (3) Soldering by soldering iron

Peak temperature (lead part temperature) 350°C or below
 Time (each pins) 3 seconds or less

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.

# (4) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

# 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

#### **USAGE CAUTIONS**

- 1. Protect against static electricity when handling.
- **2.** Avoid storage at a high temperature and high humidity.



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Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The -AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (\*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)		on contained devices	
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)	
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	Chromium < 1000 PPM Not Detected		etected	
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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